74AC11158

Part Description

This data selector/multiplexer contains inverters and drivers to supply full data selection to the four output gates. A separate output-enable ($\overline{OE}$) input is provided. A 4-bit word is selected from one of two sources and is routed to the four outputs. The 74AC11158 provides true data.

The 74AC11158 is characterized for operation from −40°C to 85°C.

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer (OCM).

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

• ISO-9001
• AS9120 certification
• Qualified Manufacturers List (QML) MIL-PRF-35835
    • Class Q Military
    • Class V Space Level
• Qualified Suppliers List of Distributors (QSLD)
    • Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

FOR REFERENCE ONLY

- **Flow-Through Architecture Optimizes PCB Layout**

- **Center-Pin V_{CC} and GND Pin Configurations Minimize High-Speed Switching Noise**

- *EPIC*™ **(Enhanced-Performance Implanted CMOS) 1-μm Process**

- **500-mA Typical Latch-Up Immunity at 125°C**

- **Package Options Include Plastic Small-Outline Packages and Standard Plastic 300-mil DIPs**

DW OR N PACKAGE
(TOP VIEW)

|			
$\overline{A}$/B	1	20	1A
1Y	2	19	1B
2Y	3	18	2A
GND	4	17	2B
GND	5	16	V_{CC}
GND	6	15	V_{CC}
GND	7	14	3A
3Y	8	13	3B
4Y	9	12	4A
$\overline{OE}$	10	11	4B

description

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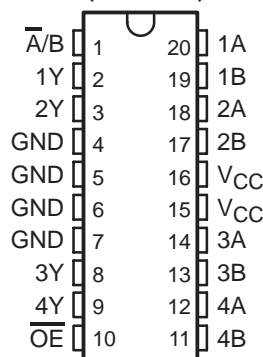The 74AC11158 is characterized for operation from –40°C to 85°C.

FUNCTION TABLE

INPUTS				OUTPUT
$\overline{OE}$	$\overline{A}$/B	A	B	Y
H	X	X	X	H
L	L	L	X	H
L	L	H	X	L
L	H	X	L	H
L	H	X	H	L

logic diagram (positive logic)
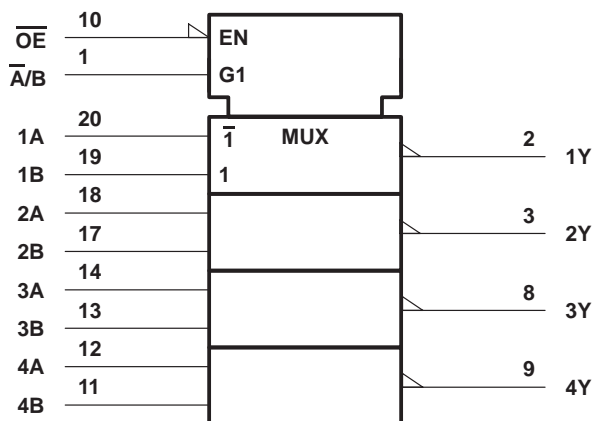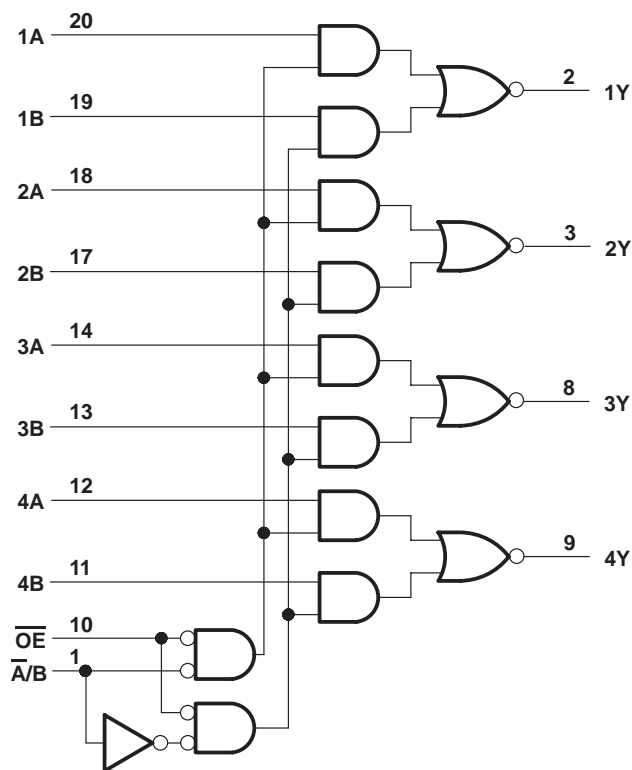
logic symbol†

† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

TEXAS INSTRUMENTS

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V_{CC} .... −0.5 V to 7 V
Input voltage range, V_I (see Note 1) .... −0.5 V to V_{CC} + 0.5 V
Output voltage range, V_O (see Note 1) .... −0.5 V to V_{CC} + 0.5 V
Input clamp current, I_{IK} (V_I < 0 or V_I > V_{CC}) .... ±20 mA
Output clamp current, I_{OK} (V_O < 0 or V_O > V_{CC}) .... ±50 mA
Continuous output current, I_O (V_O = 0 to V_{CC}) .... ±50 mA
Continuous current through V_{CC} or GND .... ±100 mA
Storage temperature range .... −65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
NOTE 1: The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

recommended operating conditions (see Note 2)

			MIN	NOM	MAX	UNIT
V_{CC}	Supply voltage		3	5	5.5	V
V_{IH}	High-level input voltage	V_{CC} = 3 V	2.1			V
		V_{CC} = 4.5 V	3.15			
		V_{CC} = 5.5 V	3.85			
V_{IL}	Low-level input voltage	V_{CC} = 3 V			0.9	V
		V_{CC} = 4.5 V			1.35	
		V_{CC} = 5.5 V			1.65	
V_I	Input voltage		0		V_{CC}	V
V_O	Output voltage		0		V_{CC}	V
I_{OH}	High-level output current	V_{CC} = 3 V			−4	mA
		V_{CC} = 4.5 V			−24	
		V_{CC} = 5.5 V			−24	
I_{OL}	Low-level output current	V_{CC} = 3 V			12	mA
		V_{CC} = 4.5 V			24	
		V_{CC} = 5.5 V			24	
Δt/Δv	Input transition rise or fall rate	$\overline{A}$/B	0		5	ns/V
		Except $\overline{A}$/B	0		10	
T_A	Operating free-air temperature		−40		85	°C

NOTE 2: Unused or floating inputs must be held high or low.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	V$_{CC}$	T$_A$ = 25°C			MIN	MAX	UNIT
			MIN	TYP	MAX			
V$_{OH}$	I$_{OH}$ = − 50 µA	3 V	2.9			2.9		V
		4.5 V	4.4			4.4		
		5.5 V	5.4			5.4		
	I$_{OH}$ = − 4 mA	3 V	2.58			2.48		
	I$_{OL}$ = − 24 mA	4.5 V	3.94			3.8		
		5.5 V	4.94			4.8		
	I$_{OH}$ = −75 mA†	5.5 V				3.85		
V$_{OL}$	I$_{OL}$ = 50 µA	3 V			0.1		0.1	V
		4.5 V			0.1		0.1	
		5.5 V			0.1		0.1	
	I$_{OL}$ = 12 mA	3 V			0.36		0.44	
	I$_{OL}$ = 24 mA	4.5 V			0.36		0.44	
		5.5 V			0.36		0.44	
	I$_{OL}$ = 75 mA†	5.5 V					1.65	
I$_I$	V$_I$ = V$_{CC}$ or GND	5.5 V			±0.1		±1	µA
I$_{CC}$	V$_I$ = V$_{CC}$ or GND, I$_O$ = 0	5.5 V			8		80	µA
C$_i$	V$_I$ = V$_{CC}$ or GND	5 V		4				pF

† Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

switching characteristics over recommended operating free-air temperature range, V$_{CC}$ = 3.3 V ± 0.3 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	T$_A$ = 25°C			MIN	MAX	UNIT
			MIN	TYP	MAX			
t$_{PLH}$	A or B	Y	1.5	5.8	8.3	1.5	9	ns
t$_{PHL}$			1.5	5.9	8.6	1.5	9.4	
t$_{PLH}$	$\overline{A}$/B	Y	1.5	6.5	9	1.5	9.8	ns
t$_{PHL}$			1.5	6.8	9.4	1.5	10.3	
t$_{PLH}$	$\overline{OE}$	Y	1.5	5.7	8.1	1.5	9	ns
t$_{PHL}$			1.5	6.3	8.8	1.5	9.7	

switching characteristics over recommended operating free-air temperature range, V$_{CC}$ = 5 V ± 0.5 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	T$_A$ = 25°C			MIN	MAX	UNIT
			MIN	TYP	MAX			
t$_{PLH}$	A or B	Y	1.5	3.6	6.4	1.5	6.8	ns
t$_{PHL}$			1.5	3.9	6.6	1.5	7.4	
t$_{PLH}$	$\overline{A}$/B	Y	1.5	4.1	6.9	1.5	7.4	ns
t$_{PHL}$			1.5	4.9	7.2	1.5	7.9	
t$_{PLH}$	$\overline{OE}$	Y	1.5	3.7	6.4	1.5	6.9	ns
t$_{PHL}$			1.5	4.2	6.9	1.5	7.5	

74AC11158
QUADRUPLE 2-LINE TO 1-LINE DATA SELECTOR/MULTIPLEXER

operating characteristics, V_{CC} = 5 V, T_A = 25°C

PARAMETER		TEST CONDITIONS	TYP	UNIT
C_{pd}	Power dissipation capacitance	C_L = 50 pF, f = 1 MHz	33	pF

PARAMETER MEASUREMENT INFORMATION

LOAD CIRCUIT

VOLTAGE WAVEFORMS
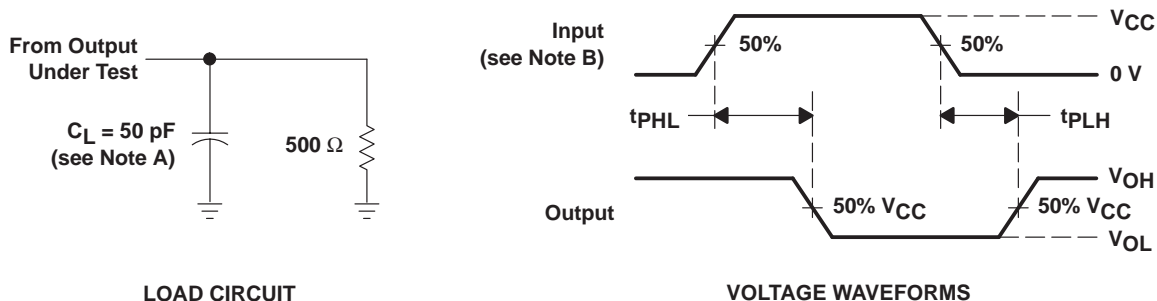
NOTES: A. C_L includes probe and jig capacitance.
B. Input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, Z_O = 50 Ω, t_r = 3 ns, t_f = 3 ns.
C. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

PACKAGING INFORMATION

Orderable Device	Status [1]	Package Type	Package Drawing	Pins	Package Qty	Eco Plan [2]	Lead/Ball Finish	MSL Peak Temp [3]
74AC11158DW	OBSOLETE	SOIC	DW	20		TBD	Call TI	Call TI
74AC11158DWR	OBSOLETE	SOIC	DW	20		TBD	Call TI	Call TI
74AC11158DWR	OBSOLETE	SOIC	DW	20		TBD	Call TI	Call TI
74AC11158N	OBSOLETE	PDIP	N	20		TBD	Call TI	Call TI
74AC11158N	OBSOLETE	PDIP	N	20		TBD	Call TI	Call TI

[1] The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

[2] Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
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[3] MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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