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April 1st, 2010 Renesas Electronics Corporation

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DATA SHEET

RENESAS

BIPOLAR ANALOG INTEGRATED CIRCUIT μ PC29xx Series

THREE TERMINAL LOW DROPOUT VOLTAGE REGULATOR

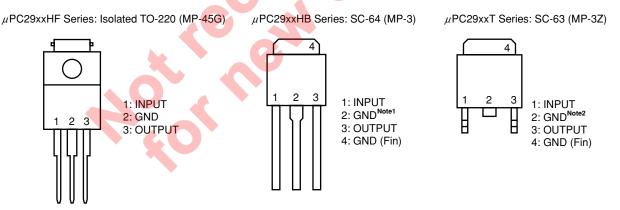
The μ PC29xx series of low dropout voltage three terminal positive regulators is constructed with PNP output transistor. The μ PC29xx series feature the ability to source 1 A of output current with a low dropout voltage of typically 0.7 V.

The power dissipation of the μ PC29xx series can be drastically reduced compared with the conventional three terminal positive voltage regulators that is constructed with NPN output transistor. Also, this series corresponds to the low voltage output (3.0 V, 3.3 V) which is not in the conventional low dropout regulators (μ PC24xxA series).

FEATURES

- Output current in excess of 1.0 A
- Low dropout voltage
 - $V_{DIF} = 0.7 V TYP. (Io = 1 A)$
- On-chip over-current and thermal protection circuit
- · On-chip output transistor safe operating area protection circuit

<R> PIN CONFIGURATIONS (Marking Side)



Notes 1. No.2 pin and No.4 fin are common GND.

2. No.2 pin is cut. No.2 pin and No.4 fin are common GND.

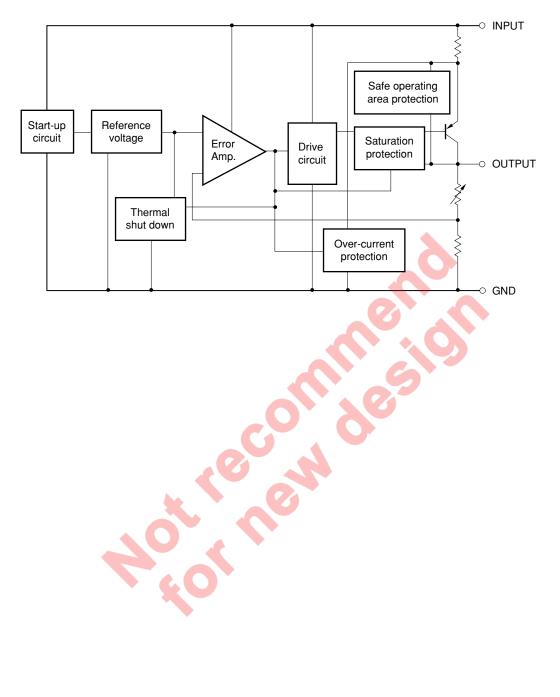
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Document No. G10026EJ4V0DS00 (4th edition) Date Published September 2007 N Printed in Japan

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The mark <R> shows major revised points. The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what." field.

BLOCK DIAGRAM



<R> ORDERING INFORMATION

Part Number	Package	Output Voltage	Marking
μPC2903HF	Isolated TO-220 (MP-45G)	3.0 V	2903
μPC2903HB	SC-64 (MP-3)	3.0 V	2903
μPC2903T	SC-63 (MP-3Z)	3.0 V	2903
μPC2933HF	Isolated TO-220 (MP-45G)	3.3 V	2933
μPC2933HB	SC-64 (MP-3)	3.3 V	2933
μPC2933T	SC-63 (MP-3Z)	3.3 V	2933
μPC2905HF	Isolated TO-220 (MP-45G)	5.0 V	2905
μPC2905HB	SC-64 (MP-3)	5.0 V	2905
μPC2905T	SC-63 (MP-3Z)	5.0 V	2905
μPC2906HF	Isolated TO-220 (MP-45G)	6.0 V	2906
μPC2906HB	SC-64 (MP-3)	6.0 V	2906
μPC2906T	SC-63 (MP-3Z)	6.0 V	2906
μPC2907HF	Isolated TO-220 (MP-45G)	7.0 V	2907
μPC2907HB	SC-64 (MP-3)	7.0 V	2907
μPC2907T	SC-63 (MP-3Z)	7.0 V	2907
μPC2908HF	Isolated TO-220 (MP-45G)	8.0 V	2908
μPC2908HB	SC-64 (MP-3)	8.0 V	2908
μPC2908T	SC-63 (MP-3Z)	8.0 V	2908
μPC2909HF	Isolated TO-220 (MP-45G)	9.0 V	2909
μPC2909HB	SC-64 (MP-3)	9.0 V	2909
μPC2909T	SC-63 (MP-3Z)	9.0 V	2909
μPC2910HF	Isolated TO-220 (MP-45G)	10.0 V	2910
μPC2910HB	SC-64 (MP-3)	10.0 V	2910
μPC2910T	SC-63 (MP-3Z)	10.0 V	2910
μPC2912HF	Isolated TO-220 (MP-45G)	12.0 V	2912
μPC2912HB	SC-64 (MP-3)	12.0 V	2912
μPC2912T	SC-63 (MP-3Z)	12.0 V	2912

Remark Tape-packaged products have the symbol -E1, or -E2 suffixed to the part number. Pb-free products have the symbol -AZ, or -AY suffixed to the part number. Refer to the following table for details.

Part Number Note1	Package	Packege Type
μPC29xxHF	Isolated TO-220 (MP-45G)	Packed in envelop
µPC29xxHF-AZ Note2	Isolated TO-220 (MP-45G)	Packed in envelop
μPC29xxHB	SC-64 (MP-3)	Packed in envelop
µPC29xxHB-AZ Note2	SC-64 (MP-3)	Packed in envelop
μPC29xxHB-AY Note3	SC-64 (MP-3)	Packed in envelop
μPC29xxT-E1	SC-63 (MP-3Z)	16 mm wide embossed taping
		Pin 1 on draw-out side
		• 2000 pcs/reel
µPC29xxT-E1-AZ Note2	SC-63 (MP-3Z)	• 16 mm wide embossed taping
		Pin 1 on draw-out side
		• 2000 pcs/reel
µPC29xxT-E1-AY Note3	SC-63 (MP-3Z)	 16 mm wide embossed taping
		Pin 1 on draw-out side
		• 2000 pcs/reel
μPC29xxT-E2	SC-63 (MP-3Z)	• 16 mm wide embossed taping
		Pin 1 at take-up side
		• 2000 pcs/reel
µPC29xxT-E2-AZ Note2	SC-63 (MP-3Z)	16 mm wide embossed taping
		Pin 1 at take-up side
		• 2000 pcs/reel
µPC29xxT-E2-AY Note3	SC-63 (MP-3Z)	• 16 mm wide embossed taping
		Pin 1 at take-up side
		• 2000 pcs/reel

Notes 1. xx stands for symbols that indicate the output voltage.

2. Pb-free (This product does not contain Pb in the external electrode.)

202

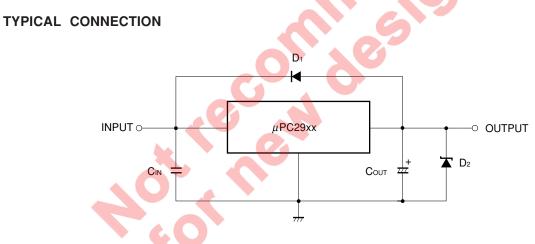
3. Pb-free (This product does not contain Pb in the external electrode, Sn100% plating.)

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, Unless otherwise specified.)

Parameter	Symbol	Ra	ting	Unit	
Parameter	Symbol	μPC29xxHF	μPC29xxHB, μPC29xxT	Unit	
Input Voltage	Vin	2	0	V	
Internal Power Dissipation (Tc = $25^{\circ}C$) Note	Р⊤	15	15 10		
Operating Ambient Temperature	Та	–30 te	o +85	°C	
Operating Junction Temperature	TJ	–30 tc	+150	°C	
Storage Temperature	Tstg	–55 to	+150	°C	
Thermal Resistance (Junction to Case)	Rth (J-C)	7 12.5		°C/W	
Thermal Resistance (Junction to Ambient)	Rth (J-A)	65	125	°C/W	

Note Internally limited. When the operating junction temperature rises above 150°C, the internal circuit shuts down the output voltage.

Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.



- CIN : 0.1 μF or higher. Be sure to connect CIN to prevent parasitic oscillation. Set this value according to the length of the line between the regulator and the INPUT pin. Use of a film capacitor or other capacitor with first-rate voltage and temperature characteristics is recommended. If using a laminated ceramic capacitor, it is necessary to ensure that CIN is 0.1 μF or higher for the voltage and temperature range to be used.
- Cout : 47 μF or higher. Be sure to connect Cout to prevent oscillation and improve excessive load regulation. Place C_{IN} and Cout as close as possible to the IC pins (within 1 to 2 cm). Also, use an electrolytic capacitor with low impedance characteristics if considering use at sub-zero temperatures.
- D1 : If the OUTPUT pin has a higher voltage than the INPUT pin, connect a diode.
- D₂ : If the OUTPUT pin has a lower voltage than the GND pin, connect a Schottky barrier diode.

Caution Make sure that no voltage is applied to the OUTPUT pin from external.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Type Number	MIN.	TYP.	MAX.	Unit
Input Voltage	VIN	μPC2903	4.0		16	
		μPC2933	4.3		16	
		μPC2905	6		16	
		μPC2906	7		16	
		μPC2907	8		16	V
		μPC2908	9		18	
		μPC2909	10		18	
		μPC2910	11		18	
		μPC2912	13		18	
Output Current	lo	all	0		1.0	А
Operating Ambient Temperature	Та	all	-30		+85	°C
Operating Junction Temperature	TJ	all	-30		+125	°C

ELECTRICAL CHARACTERISTICS

 μ PC2903 (T_J = 25°C, V_{IN} = 5 V, Io = 500 mA, C_{IN} = 0.22 μ F, Cout = 47 μ F, unless otherwise specified.)

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		2.88	3.0	3.12	
		$\begin{array}{l} 0^{\circ}C \leq T_{J} \leq 125^{\circ}C, \ 4.0 \ V \leq V_{IN} \leq 16 \ V, \\ 0 \ A \leq I_{O} \leq 500 \ mA \end{array}$	2.85		3.15	V
Line Regulation		$4.0 \text{ V} \leq \text{V}_{IN} \leq 16 \text{ V}$		11	30	mV
Load Regulation	REGL	0 A ≤ lo ≤ 1 A		9	30	mV
Quiescent Current	Ibias	lo = 0 A		1.9	4.0	
	0	lo = 1 A		23	60	mA
Startup Quiescent Current	IBIAS (s)	VIN = 2.95 V, Io = 0 A		12	30	mA
	60	Vin = 2.95 V, lo = 1 A			80	ma
Quiescent Current Change	Δ Ibias	$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ 4.0 \ V \leq V_{IN} \leq 16 \ V$		3.2	20	mA
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		52		μVr.m.s.
Ripple Rejection	R∙R	f = 120 Hz, 4.0 V \leq VIN \leq 16 V	48	63		dB
Dropout Voltage	VDIF	$0^{\circ}C \le T_J \le 125^{\circ}C$, $I_0 = 1 A$		0.7	1.0	V
Short Circuit Current	IO short	VIN = 4.5 V	1.2	1.7	3.0	
		VIN = 16 V		1.2		A
Peak Output Current	IO peak	VIN = 4.5 V	1.0	1.5	3.0	Α
		VIN = 16 V	1.3	1.7	2.8	
Temperature Coefficient of Output Voltage	ΔVo/ΔT	$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ \text{lo} = 5 \ \text{mA}$		-0.5		mV/°C

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		3.17	3.3	3.43	
		$\label{eq:constraint} \begin{array}{l} 0^\circ C \leq T_J \leq 125^\circ C, 4.3 \ V \leq V_{IN} \leq 16 \ V, \\ 0 \ A \leq I_O \leq 500 \ mA \end{array}$	3.14		3.46	v
		$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ 0 \ A \leq I_0 \leq 1 \ A$				
Line Regulation	REGIN	$4.3 \ V \leq V_{IN} \leq 16 \ V$		12	33	mV
Load Regulation	REG∟	$0 A \le I_0 \le 1 A$		23	33	mV
Quiescent Current	Ibias	lo = 0 A		2.0	4.0	
		lo = 1 A		30	60	– mA
Startup Quiescent Current	IBIAS (s)	V _{IN} = 3.1 V, Io = 0 A		10	30	
		V _{IN} = 3.1 V, Io = 1 A			80	mA
Quiescent Current Change		$0^{\circ}C \leq T_{J} \leq 125^{\circ}C, 4.3 V \leq V_{IN} \leq 16 V$		3.0	20	mA
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		55		μVr.m.s.
Ripple Rejection	R∙R	$f = 120 \text{ Hz}, 4.3 \text{ V} \le \text{Vin} \le 16 \text{ V}$	48	64		dB
Dropout Voltage	VDIF	$0^{\circ}C \leq T_J \leq 125^{\circ}C$, Io = 1 A	5	0.7	1.0	V
Short Circuit Current	IO short	V _{IN} = 4.5 V	1.2	1.6	3.0	
		VIN = 16 V		1.2		A
Peak Output Current	IO peak	V _{IN} = 4.5 V	1.0	1.4	3.0	
		VIN = 16 V	1.3	1.7	2.8	.8 A
Temperature Coefficient of Output Voltage	ΔVο/ΔΤ	$0^{\circ}C \leq T_J \leq 125^{\circ}C$, $I_0 = 5 \text{ mA}$		-0.4		mV/°C

μ PC2933 (T_J = 25°C, V_{IN} = 5 V, Io = 500 mA, C_{IN} = 0.22 μ F, Cout = 47 μ F, unless otherwise specified.)

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		4.8	5.0	5.2	
		$\label{eq:constraint} \begin{array}{l} 0^\circ C \leq T_J \leq 125^\circ C, \ 6 \ V \leq V_{IN} \leq 16 \ V, \\ 0 \ A \leq I_O \leq 500 \ mA \end{array}$	4.75		5.25	v
		$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ 0 \ A \leq I_O \leq 1 \ A$				
Line Regulation	REGIN	$6 V \le V_{IN} \le 16 V$		23	50	mV
Load Regulation	REG∟	$0 A \le I_0 \le 1 A$		28	50	mV
Quiescent Current	Ibias	lo = 0 A		2.2	4.0	
		lo = 1 A		30	60	– mA
Startup Quiescent Current	IBIAS (s)	V _{IN} = 4.5 V, Io = 0 A		10	30	
		$V_{IN} = 4.5 V, I_{O} = 1 A$			80	mA
Quiescent Current Change	ΔIbias	$0^\circ C \leq T_J \leq 125^\circ C, \ 6 \ V \leq V_{IN} \leq 16 \ V$		2.9	20	mA
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		90		μV r.m.s.
Ripple Rejection	R∙R	f = 120 Hz, 6 V ≤ ViN ≤ 16 V	46	61		dB
Dropout Voltage	VDIF	0°C ≤ TJ ≤ 125°C, lo = 1 A		0.7	1.0	V
Short Circuit Current	IO short	VIN = 6.5 V	1.15	1.8	3.0	
		V _{IN} = 16 V		1.1		A
Peak Output Current	IO peak	VIN = 6.5 V	1.1	1.5	3.0	— A
		VIN = 16 V	1.4	2.0	2.8	
Temperature Coefficient of Output Voltage	$\Delta Vo/\Delta T$	$0^{\circ}C \leq T_{J} \leq 125^{\circ}C$, $I_{O} = 5 \text{ mA}$		0.6		mV/°C

μ PC2905 (T_J = 25°C, V_{IN} = 8 V, Io = 500 mA, C_{IN} = 0.22 μ F, Cout = 47 μ F, unless otherwise specified.)

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		5.76	6.0	6.24	
		$\label{eq:constraint} \begin{array}{l} 0^\circ C \leq T_J \leq 125^\circ C, \ 7 \ V \leq V_{IN} \leq 16 \ V, \\ 0 \ A \leq I_O \leq 500 \ mA \end{array}$	5.70		6.30	v
		$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ 0 \ A \leq I_0 \leq 1 \ A$				
Line Regulation	REGIN	$7 \text{ V} \leq V_{IN} \leq 16 \text{ V}$		25	60	mV
Load Regulation	REG∟	$0 A \le I_0 \le 1 A$		29	60	mV
Quiescent Current	Ibias	Io = 0 A		2.0	4.0	
		lo = 1 A		23	60	– mA
Startup Quiescent Current	BIAS (s)	$V_{IN} = 5.5 V$, Io = 0 A		10	30	
		V _{IN} = 5.5 V, Io = 1 A			80	mA
Quiescent Current Change	ΔI_BIAS	$0^{\circ}C \leq T_{J} \leq 125^{\circ}C, 7 V \leq V_{IN} \leq 16 V$		2.2	20	mA
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		108		μVr.m.s.
Ripple Rejection	R∙R	f = 120 Hz, 7 V ≤ VIN ≤ 16 V	44	60		dB
Dropout Voltage	VDIF	$0^{\circ}C \leq T_J \leq 125^{\circ}C$, $I_0 = 1 A$		0.7	1.0	V
Short Circuit Current	IO short	V _{IN} = 7.5 V		1.8		
		V _{IN} = 16 V	2	1.1		A
Peak Output Current	IO peak	V _{IN} = 7.5 V	1.1	1.5	3.0	•
		VIN = 16 V	1.4	2.0	2.8	A
Temperature Coefficient of Output Voltage	ΔVο/ΔΤ	$0^{\circ}C \le T_J \le 125^{\circ}C$, $I_0 = 5 \text{ mA}$		0.6		mV/°C

μ PC2906 (T_J = 25°C, V_{IN} = 9 V, Io = 500 mA, C_{IN} = 0.22 μ F, Cout = 47 μ F, unless otherwise specified.)

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		6.72	7.0	7.28	
		$\label{eq:constraint} \begin{array}{l} 0^\circ C \leq T_J \leq 125^\circ C, \ 8 \ V \leq V_{IN} \leq 16 \ V, \\ 0 \ A \leq I_O \leq 500 \ mA \end{array}$	6.65		7.35	v
		$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ 0 \ A \leq I_O \leq 1 \ A$				
Line Regulation	REGIN	$8 V \le V_{IN} \le 16 V$		27	70	mV
Load Regulation	REG∟	$0 A \le I_0 \le 1 A$		30	70	mV
Quiescent Current	Ibias	lo = 0 A		2.0	4.0	
		lo = 1 A		24	60	— mA
Startup Quiescent Current	IBIAS (s)	$V_{IN} = 6.5 V, I_{O} = 0 A$		10	30	
		$V_{IN} = 6.5 V, I_{O} = 1 A$			80	mA
Quiescent Current Change	ΔIbias	$0^\circ C \leq T_J \leq 125^\circ C, \ 8 \ V \leq V_{IN} \leq 16 \ V$		2.3	20	mA
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		126		μV r.m.s.
Ripple Rejection	R∙R	f = 120 Hz, 8 V ≤ ViN ≤ 16 V	43	59		dB
Dropout Voltage	VDIF	$0^{\circ}C \le T_{J} \le 125^{\circ}C$, $I_{O} = 1 A$		0.7	1.0	V
Short Circuit Current	IO short	VIN = 8.5 V		1.8		
		V _{IN} = 16 V		1.1		A
Peak Output Current	IO peak	VIN = 8.5 V	1.1	1.5	3.0	A
		VIN = 16 V	1.4	2.0	2.8	
Temperature Coefficient of Output Voltage	$\Delta Vo/\Delta T$	$0^{\circ}C \leq T_{J} \leq 125^{\circ}C$, $I_{O} = 5 \text{ mA}$		0.6		mV/°C

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μ PC2907 (T_J = 25°C, V_{IN} = 10 V, Io = 500 mA, C_{IN} = 0.22 μ F, Cout = 47 μ F, unless otherwise specified.)

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		7.68	8.0	8.32	
		$\label{eq:constraint} \begin{array}{l} 0^\circ C \leq T_J \leq 125^\circ C, \; 9 \; V \leq V_{IN} \leq 18 \; V, \\ 0 \; A \leq I_O \leq 500 \; mA \end{array}$	7.6		8.4	V
		$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ 0 \ A \leq I_0 \leq 1 \ A$				
Line Regulation	REGIN	$9 V \le V_{IN} \le 18 V$		31	80	mV
Load Regulation	REG∟	$0 A \le I_0 \le 1 A$		30	80	mV
Quiescent Current	Ibias	Io = 0 A		1.9	4.0	
		lo = 1 A		25	60	– mA
Startup Quiescent Current	IBIAS (s)	$V_{IN} = 7.5 V$, Io = 0 A		10	30	
		$V_{IN} = 7.5 V$, Io = 1 A			80	mA
Quiescent Current Change	ΔΙβιάς	$0^{\circ}C \leq T_{J} \leq 125^{\circ}C, 9 V \leq V_{IN} \leq 18 V$		2.4	20	mA
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		145		μV r.m.s.
Ripple Rejection	R∙R	f = 120 Hz, 9 V ≤ V _{IN} ≤ 18 V	42	58		dB
Dropout Voltage	VDIF	0°C ≤ TJ ≤ 125°C, Io = 1 A	5	0.7	1.0	V
Short Circuit Current	IO short	V _{IN} = 9.5 V		1.9		•
		V _{IN} = 18 V		1.0		A
Peak Output Current	IO peak	VIN = 9.5 V	1.1	1.5	3.0	
		VIN = 18 V	1.4	2.0	2.8	A
Temperature Coefficient of Output Voltage	ΔVο/ΔΤ	$0^{\circ}C \le T_{J} \le 125^{\circ}C$, $I_{O} = 5 \text{ mA}$		0.6		mV/°C

Not new

μ PC2908 (T_J = 25°C, V_{IN} = 11 V, Io = 500 mA, C_{IN} = 0.22 μ F, Cout = 47 μ F, unless otherwise specified.)

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		8.64	9.0	9.36	
		$\label{eq:constraint} \begin{array}{l} 0^\circ C \leq T_J \leq 125^\circ C, \ 10 \ V \leq V_{IN} \leq 18 \ V, \\ 0 \ A \leq I_O \leq 500 \ mA \end{array}$	8.55		9.45	v
		$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ 0 \ A \leq I_O \leq 1 \ A$				
Line Regulation	REGIN	$10 V \le V_{IN} \le 18 V$		31	90	mV
Load Regulation	REG∟	$0 A \le I_0 \le 1 A$		32	90	mV
Quiescent Current	Ibias	lo = 0 A		1.9	4.0	
		lo = 1 A		27	60	— mA
Startup Quiescent Current	IBIAS (s)	VIN = 8.5 V, Io = 0 A		11	30	
		VIN = 8.5 V, Io = 1 A			80 mA	mA
Quiescent Current Change	ΔI_BIAS	$0^{\circ}C \leq T_{\rm J} \leq 125^{\circ}C, \ 10 \ V \leq V_{\rm IN} \leq 18 \ V \label{eq:VIN}$		3.0	20	mA
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		155		$\mu V_{r.m.s.}$
Ripple Rejection	R∙R	f = 120 Hz, 10 V ≤ ViN ≤ 18 V	41	58		dB
Dropout Voltage	VDIF	0°C ≤ TJ ≤ 125°C, lo = 1 A		0.7	1.0	V
Short Circuit Current	IO short	V _{IN} = 10.5 V		1.9		•
		V _{IN} = 18 V		1.0		A
Peak Output Current	IO peak	V _{IN} = 10.5 V	1.1	1.5	3.0	- A
		VIN = 18 V	1.4	2.0	3.0	
Temperature Coefficient of Output Voltage	$\Delta Vo/\Delta T$	$0^{\circ}C \leq T_{J} \leq 125^{\circ}C$, $I_{O} = 5 \text{ mA}$		1.0		mV/°C

N°C LEW

μ PC2909 (T_J = 25°C, V_{IN} = 12 V, Io = 500 mA, C_{IN} = 0.22 μ F, Cout = 47 μ F, unless otherwise specified.)

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		9.6	10.0	10.4	
		$\label{eq:constraint} \begin{array}{l} 0^\circ C \leq T_J \leq 125^\circ C, \ 11 \ V \leq V_{IN} \leq 18 \ V, \\ 0 \ A \leq I_O \leq 500 \ mA \end{array}$	9.5		10.5	v
		$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ 0 \ A \leq I_0 \leq 1 \ A$				
Line Regulation	REGIN	$11 \ V \le V_{IN} \le 18 \ V$		35	100	mV
Load Regulation	REG∟	$0 A \le I_0 \le 1 A$		33	100	mV
Quiescent Current	Ibias	lo = 0 A		2.0	4.0	
		lo = 1 A		25	60	– mA
Startup Quiescent Current	IBIAS (s)	$V_{IN} = 9.5 V, I_{O} = 0 A$		10	30	
		$V_{IN} = 9.5 V, I_{O} = 1 A$			80	mA
Quiescent Current Change	ΔΙβιάς	$0^{\circ}C \le T_J \le 125^{\circ}C$, 11 V $\le V_{IN} \le 18$ V		1.9	20	mA
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		180		μVr.m.s.
Ripple Rejection	R∙R	f = 120 Hz, 11 V ≤ ViN ≤ 18 V	40	56		dB
Dropout Voltage	VDIF	0°C ≤ TJ ≤ 125°C, Io = 1 A	S	0.7	1.0	V
Short Circuit Current	IO short	V _{IN} = 11.5 V		1.7		
		VIN = 18 V		1.0		A
Peak Output Current	IO peak	V _{IN} = 11.5 V	1.1	1.6	3.0	
		VIN = 18 V	1.4	2.0	3.0	A
Temperature Coefficient of Output Voltage	ΔVο/ΔΤ	$0^{\circ}C \le T_{J} \le 125^{\circ}C$, $I_{O} = 5 \text{ mA}$		2.1		mV/°C

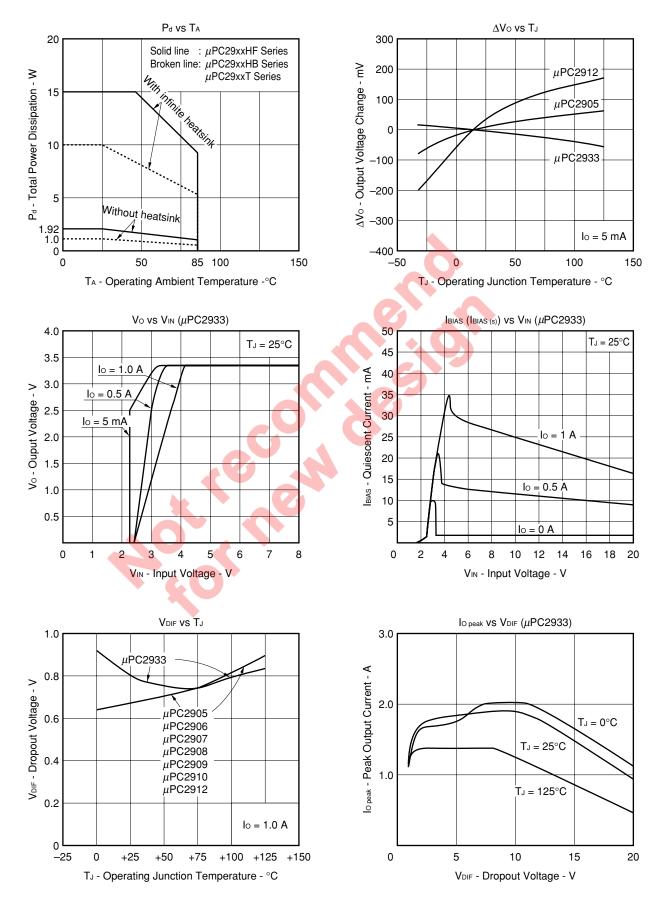
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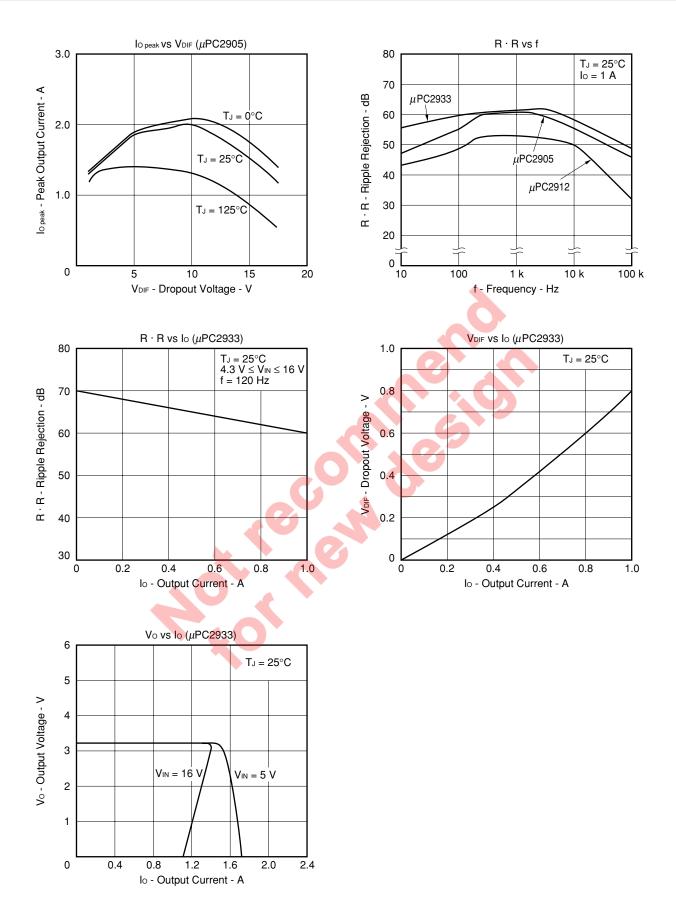
μ PC2910 (T_J = 25°C, V_{IN} = 13 V, Io = 500 mA, C_{IN} = 0.22 μ F, Cout = 47 μ F, unless otherwise specified.)

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		11.52	12	12.48	
		$\label{eq:constraint} \begin{split} 0^\circ C &\leq T_J \leq 125^\circ C, \ 13 \ V \leq V_{IN} \leq 18 \ V, \\ 0 \ A \leq I_O \leq 500 \ mA \end{split}$	11.4		12.6	v
		$0^{\circ}C \leq T_{\text{J}} \leq 125^{\circ}C, \ 0 \ A \leq I_{\text{O}} \leq 1 \ A$				
Line Regulation	REGIN	$13 \text{ V} \leq \text{V}_{IN} \leq 18 \text{ V}$		38	120	mV
Load Regulation	REG∟	$0 A \le I_0 \le 1 A$		35	120	mV
Quiescent Current	Ibias	lo = 0 A		2.1	4.0	
		lo = 1 A		26	60	mA
Startup Quiescent Current	IBIAS (s)	V _{IN} = 11.5 V, Io = 0 A		10	30	
		V _{IN} = 11.5 V, Io = 1 A			80	mA
Quiescent Current Change	ΔΙβιάς	$0^{\circ}C \leq T_J \leq 125^{\circ}C, \ 13 \ V \leq V_{IN} \leq 18 \ V$		1.5	20	mA
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz	0	210		μV r.m.s.
Ripple Rejection	R∙R	f = 120 Hz, 13 V ≤ ViN ≤ 18 V	40	52		dB
Dropout Voltage	VDIF	$0^{\circ}C \le T_J \le 125^{\circ}C$, $I_0 = 1 A$		0.7	1.0	V
Short Circuit Current	IO short	V _{IN} = 14 V		1.7		
		V _{IN} = 18 V		1.0		A
Peak Output Current	IO peak	V _{IN} = 14 V	1.1	1.6	3.0	
		V _{IN} = 18 V	1.4	2.0	3.0	A
Temperature Coefficient of Output Voltage	$\Delta Vo/\Delta T$	$0^{\circ}C \leq T_{J} \leq 125^{\circ}C$, $I_{O} = 5 \text{ mA}$		2.1		mV/°C

μ PC2912 (T_J = 25°C, V_{IN} = 15 V, Io = 500 mA, C_{IN} = 0.22 μ F, Cout = 47 μ F, unless otherwise specified.)

TYPICAL CHARACTERISTICS

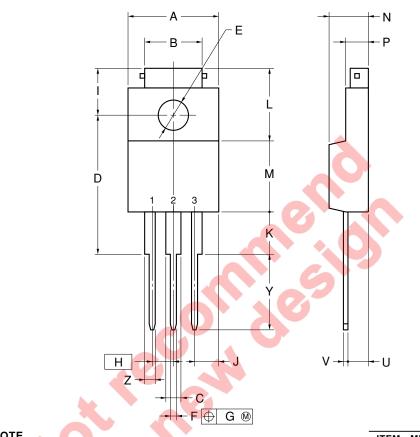




PACKAGE DRAWINGS

 μ PC29xxHF Series

3PIN PLASTIC SIP (MP-45G)



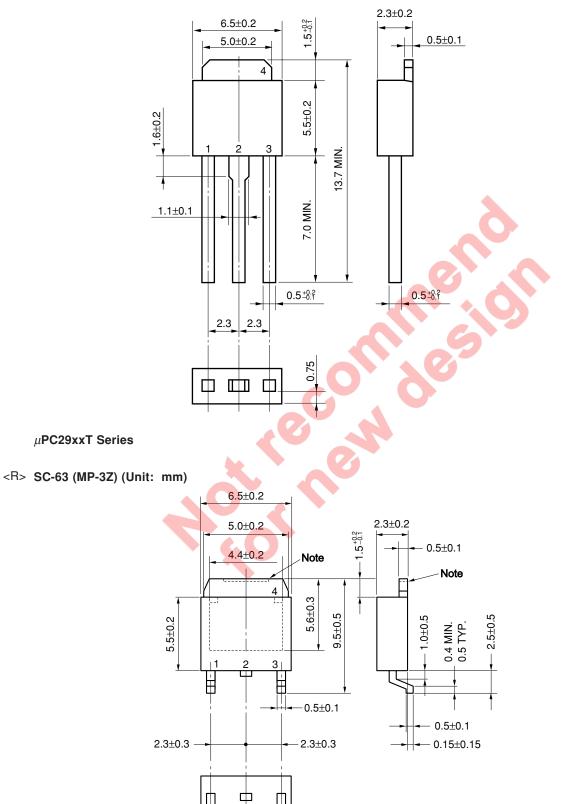
NOTE

Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
A	10.0±0.2
В	7.0±0.2
С	1.50±0.2
D	17.0±0.3
E	\$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$ \$\$
F	0.75±0.10
G	0.25
Н	2.54 (T.P.)
I	5.0±0.3
J	2.46±0.2
К	5.0±0.2
L	8.5±0.2
М	8.5±0.2
Ν	4.5±0.2
Р	2.8±0.2
U	2.4±0.5
V	0.65±0.10
Y	8.9±0.7
Z	1.30±0.2
	P3HF-254B-4

μ PC29xxHB Series

SC-64 (MP-3) (Unit: mm)



Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

<R> RECOMMENDED SOLDERING CONDITIONS

When soldering this product, it is highly recommended to observe the conditions as shown below. If other soldering processes are used, or if the soldering is performed under different condition, please make sure to consult with our sales offices.

For more details, refer to the **Semiconductor Device Mount Manual** (http://www.necel.com/pkg/en/mount/index.html)

Surface mount devices

μ PC29xxT	Series:	SC-63	(MP-3Z)
	0011001	00 00	(0=)

Process	Conditions	Symbol
Infrared ray reflow	Peak temperature: 235°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 3 times or less.	IR35-00-3
VPS	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 3 times or less.	VP15-00-3
Partial heating method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (Per each side of the device).	P350

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

Remark Flux: Rosin-based flux sith low chlorine content (chlorine 0.2 Wt% or below) is recommended.

μPC29xxT-AZ Series Note1, μPC29xxT-AY Series Note2: SC-63 (MP-3Z)

Process	Conditions	Symbol
Infrared ray reflow	Peak temperature: 260°C or below (Package surface temperature), Reflow time: 60 seconds or less (at 220°C or higher), Maximum number of reflow processes: 3 times or less.	IR60-00-3
Partial heating method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (Per each side of the device).	P350

Notes 1. Pb-free (This product does not contain Pb in the external electrode.)

2. Pb-free (This product does not contain Pb in the external electrode, Sn100% plating.)

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

Remark Flux: Rosin-based flux sith low chlorine content (chlorine 0.2 Wt% or below) is recommended.

Through-hole devices

μPC29xxHF Series, μPC29xxHF-AZ Series ^{Note1}: Isolated TO-220 (MP-45G) μPC29xxHB Series, μPC29xxHB-AZ Series ^{Note1}, μPC29xxHB-AY Series ^{Note2}: SC-64 (MP-3)

Process	Conditions	Symbol
Wave soldering (only to leads)	Solder temperature: 260°C or below, Flow time: 10 seconds or less.	WS60-00-1
Partial heating method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (Per each pin).	P350

Notes 1. Pb-free (This product does not contain Pb in the external electrode.)

2. Pb-free (This product does not contain Pb in the external electrode, Sn100% plating.)

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

CAUTION ON USE

When using the μ PC29xx series at the input voltage which is lower than in the recommended operating condition, the high quiescent current flows through devices because the transistor of the output paragraph is saturated (Refer to "IBIAS (IBIAS (s)) **vs** VIN **curves in TYPICAL CHARACTERISTICS**"). The μ PC29xx series have saturation protection circuits, but they sometimes need about 80 mA current. Therefore the power supply on the input needs the enough current capacity to pass this quiescent current when the devices startup.

<R> REFERENCE DOCUMENTS

USER'S MANUAL USAGE OF THREE TERMINAL REGULATORS REVIEW OF QUALITY AND RELIABILITY HANDBOOK INFORMATION VOLTAGE REGULATOR OF SMD SEMICONDUCTOR DEVICE MOUNT MANUAL

Document No.G12702E Document No.C12769E Document No.G11872E http://www.necel.com/pkg/en/mount/index.html

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