

SANYO Semiconductors

DATA SHEET

N-Channel Silicon MOSFET

BFL4007 — General-Purpose Switching Device Applications

Features

- Reverse recovery time t_{rr}=95ns (typ)
- Input capacitance Ciss=1200pF (typ)
- ON-resistance RDS(on)= 0.52Ω (typ)
- · 10V drive

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		600	V
Gate-to-Source Voltage	V _{GSS}		±30	V
Drain Current (DC)	IDc*1	Limited only by maximum temperature Tch=150°C	14	А
	I _{Dpack} *2	Tc=25°C (SANYO's ideal heat dissipation condition)*3	8.7	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	49	Α
Source-to-Drain Diode Forward Current (DC)	IS		14	А
Source-to-Drain Diode Forward Current (Pulse)	ISP	PW≤10μs, duty cycle≤1%	49	А
Allowable Power Dissipation	PD		2.0	W
		Tc=25°C (SANYO's ideal heat dissipation condition*)3	40	W

Note:*1 Shows chip capability

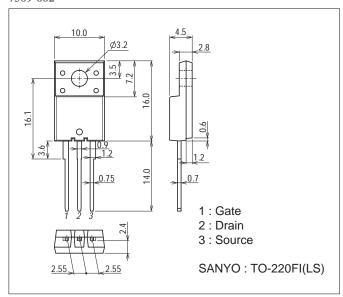
*2 Package limited

*3 SANYO's condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

Package Dimensions

unit : mm (typ) 7509-002



Product & Package Information

• Package : TO-220FI(LS)

• JEITA, JEDEC : SC-67, SOT-186A, TO-220F

Continued on next page.

• Minimum Packing Quantity : 100/bag, 50/magazine

Marking



Continued from preceding page.

Parameter	Symbol	Conditions	Ratings	Unit
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *4	EAS		215	mJ
Avalanche Current *5	IAV		8.5	А

Note: *4 VDD=99V, L=5mH, IAV=8.5A (Fig.1)

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =10mA, V _{GS} =0V	600			V
Zero-Gate Voltage Drain Current	IDSS	VDS=480V, VGS=0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±30V, V _{DS} =0V			±100	nA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	3		5	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =7A	4.3	8.5		S
Static Drain-to-Source On-State Resistance	RDS(on)	I _D =7A, V _G S=10V		0.52	0.68	Ω
Input Capacitance	Ciss	V _{DS} =30V, f=1MHz		1200		pF
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		220		pF
Reverse Transfer Capacitance	Crss	V _{DS} =30V, f=1MHz		43		pF
Turn-ON Delay Time	t _d (on)	See Fig.2		27		ns
Rise Time	tr	See Fig.2		72		ns
Turn-OFF Delay Time	t _d (off)	See Fig.2		122		ns
Fall Time	t _f	See Fig.2		48		ns
Total Gate Charge	Qg	V _{DS} =200V, V _{GS} =10V, I _D =14A		46		nC
Gate-to-Source Charge	Qgs	V _{DS} =200V, V _{GS} =10V, I _D =14A		8.6		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =200V, V _{GS} =10V, I _D =14A		26.4		nC
Diode Forward Voltage	V _{SD}	I _S =14A, V _{GS} =0V		1.1	1.5	V
Reverse Recovery Time	t _{rr}	See Fig.3		95		ns
Reverse Recovery Charge	Qrr	I _S =14A, V _{GS} =0V, di/dt=100A/μs		250		nC

Fig.1 Avalanche Resistance Test Circuit

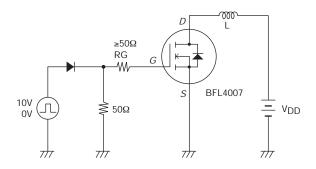
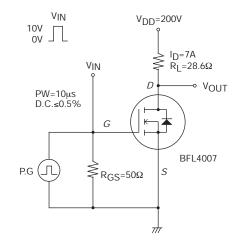
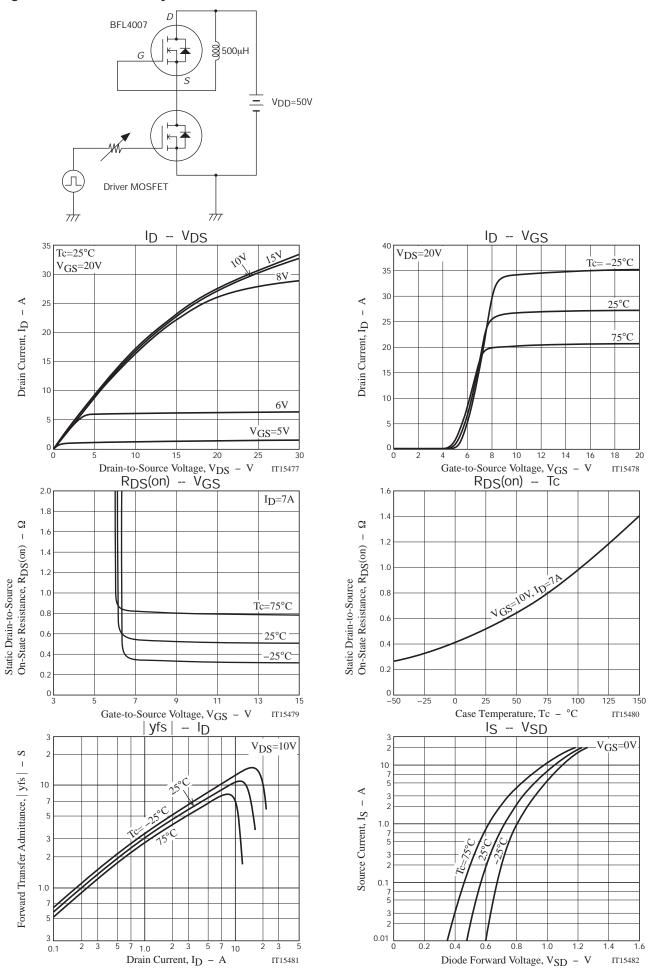


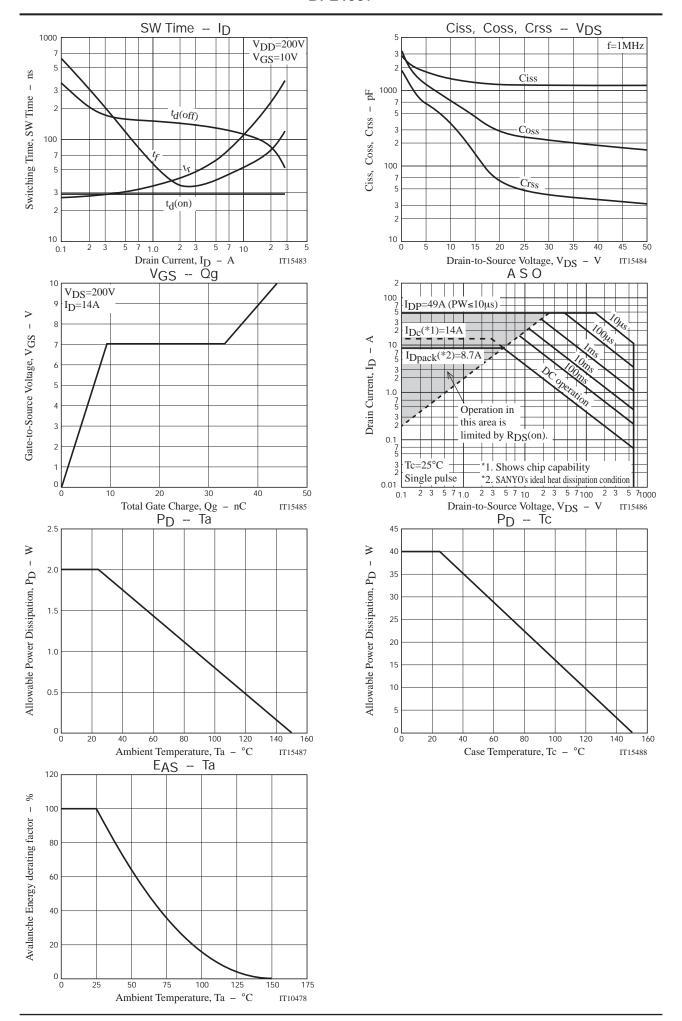
Fig.2 Switching Time Test Circuit



^{*5} L≤5mH, single pulse

Fig.3 trr Reverse Recovery Resistance Test Circuit





Note on usage : Since the BFL4007 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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